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Sheet 1

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known			
Application Number	10/634,268		
Filing Date	August 5, 2003		
First Named Inventor	Jin-Man Han		
Art Unit			
Examiner Name			
Attorney Docket Number	P2341-C-1		

			U. S. PATENT	DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ^{2 (f known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant 2,044 Figures Appearus Cus Cus
DL		US- 2002/0054502	05-2002	King, Tsu-Jae	7.47
1	-	^{US-} 6,512,690	01-2003	Qi et al.	
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	FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages		
		Country Code ³ "Number ⁴ "Kind Code ⁵ (if known)	MM-DD-YYYY		Or Relevant Figures Appear	76	
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Examiner Signature	F17-	Date Considered	4/04

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				Application Number	10/634,268	
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STA	STATEMENT BY APPLICANT			First Named Inventor	Jin-Man Han	
				Art Unit		
(Use as many sheets as necessary)			iecessary)	Examiner Name		
Sheet	2	of	2	Attorney Docket Number	P2341-C-1	

	1	NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
pL		HIRANDO, H., et al., 2V/100-ns 1T/1C Nonvolatile Ferroelectric Memory Architecture with Bitline-Driven Read Scheme and Nonrelaxation Reference Cell, IEEE Journal of Solid-State Circuits, 1997, pages 649-54, Vol. 32, No. 5	
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Signature	()01	Considered	1 4/17

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